

ABSTRACT OF THE DISCLOSURE

09859484-051801

A first substrate, a second substrate opposing to the first substrate, and liquid crystal provided in a space between the first substrate and second substrate are provided to an IPS mode liquid crystal display. A plurality of gate lines and a plurality of drain lines, thin film transistors, pixel electrodes, and common electrodes are formed on a transparent substrate in a first substrate. Each of the thin film transistors is provided in a vicinity of the intersection of the gate line and drain line. Each of the pixel electrodes is formed within a pixel region enclosed with an adjacent pair of the gate lines and an adjacent pair of the drain lines. Each of the common electrodes develops an electric field within each of the pixel regions between the pixel electrode and itself. The second substrate includes color layers provided for each of the pixel regions. The color layers are spaced apart from the gate lines and drain lines when seen on a plane.